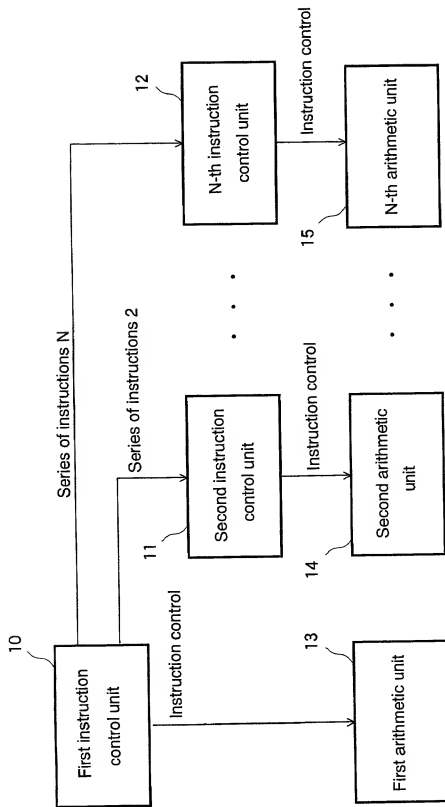


FIG. 1



F I G . 2

Second instruction memory

First instruction memory

Address	Data
0000	Series of instructions 1
0001	Series of instructions 2
0002	Series of instructions 1
0003	Series of instructions 2 (Independent execution switching)
0004	Series of instructions 1
0005	Series of instructions 1
0006	Series of instructions 1
0007	Series of instructions 1
0008	Series of instructions 2

Address	Data
0000	Series of instructions 2+
0001	Series of instructions 2+
0002	Series of instructions 2+ (Synchronous execution switching)

F I G. 3

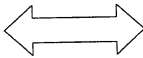
<p>Fine granularity</p>  <p>Coarse granularity</p>	Instruction level	<ul style="list-style-type: none"> <li>• ADD A,B,C : Addition</li> <li>• MUL A,B,C : Multiplication</li> </ul>
	Function level	<ul style="list-style-type: none"> <li>• <math>\text{fft}(x,y)</math> : Fourier transform</li> <li>• <math>\text{fir}(x,y)</math> : FIR filter</li> </ul>
	Application level	<ul style="list-style-type: none"> <li>• MPEG Video (Image CODEC)</li> <li>• GSM-AMR(Voice CODEC)</li> </ul>
	System level	<ul style="list-style-type: none"> <li>• Portable terminal system</li> </ul>

FIG. 4

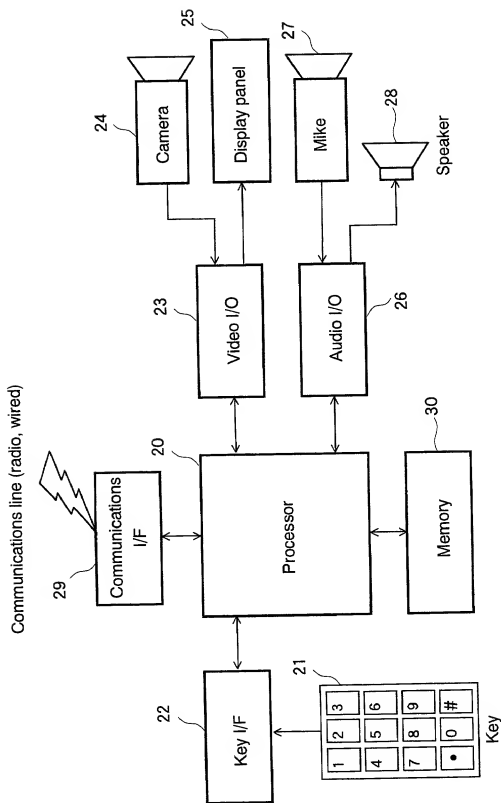
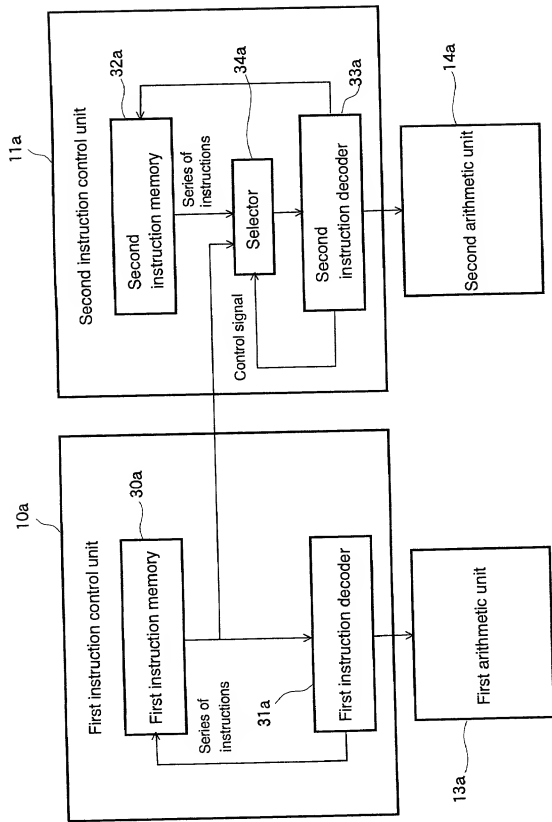


FIG. 5



F I G. 6

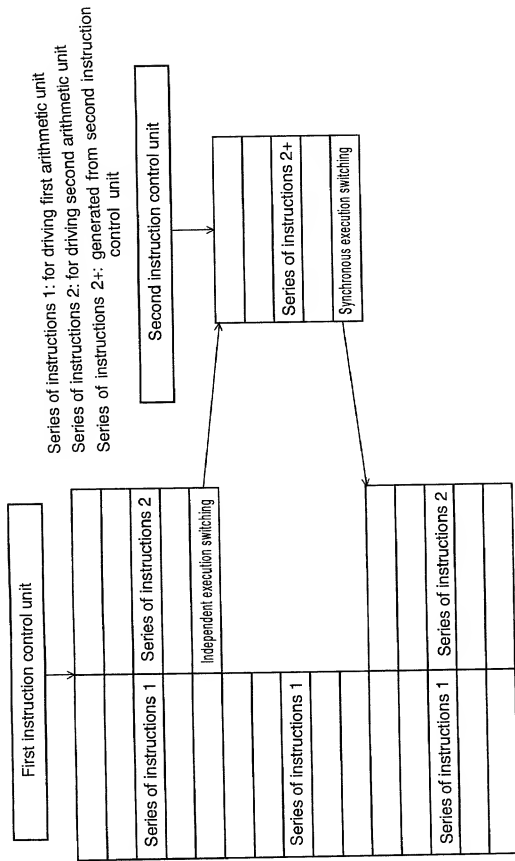
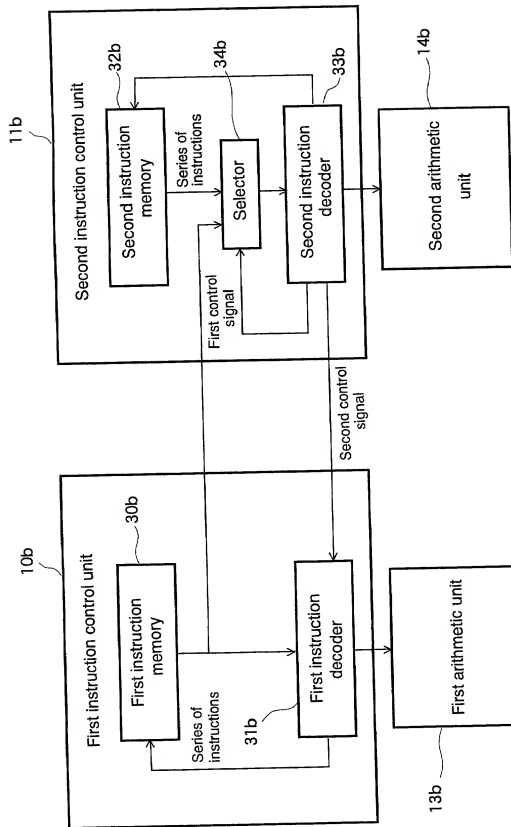


FIG. 7



F I G . 8

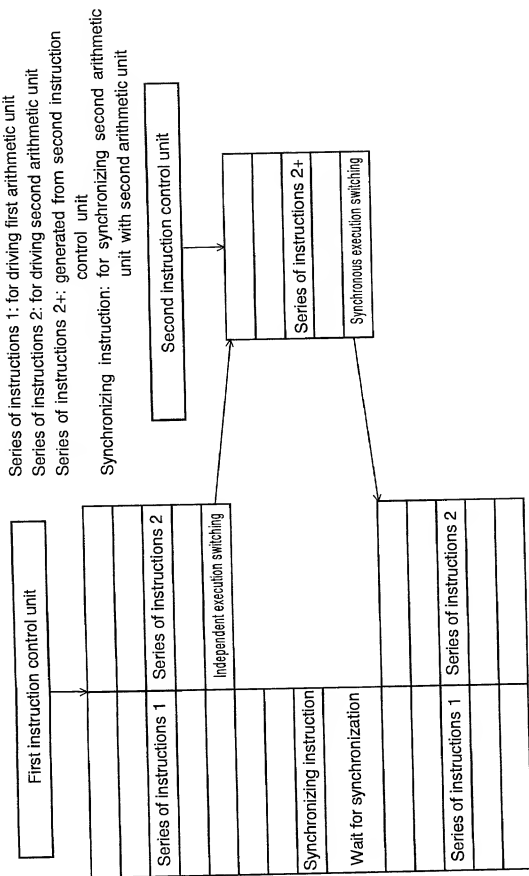




FIG. 9

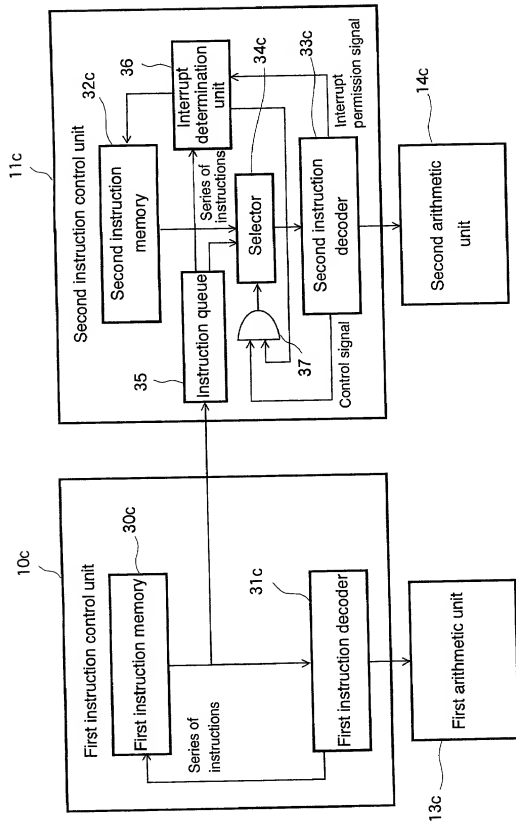


FIG. 10

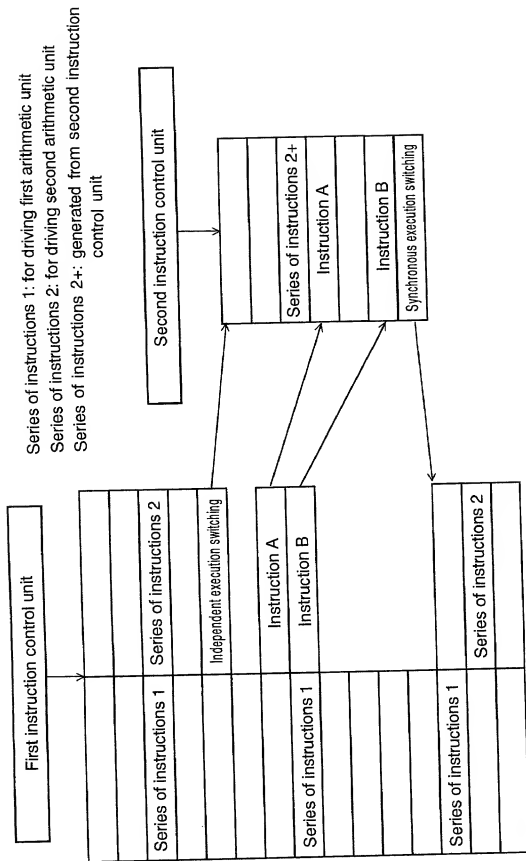


FIG. 11

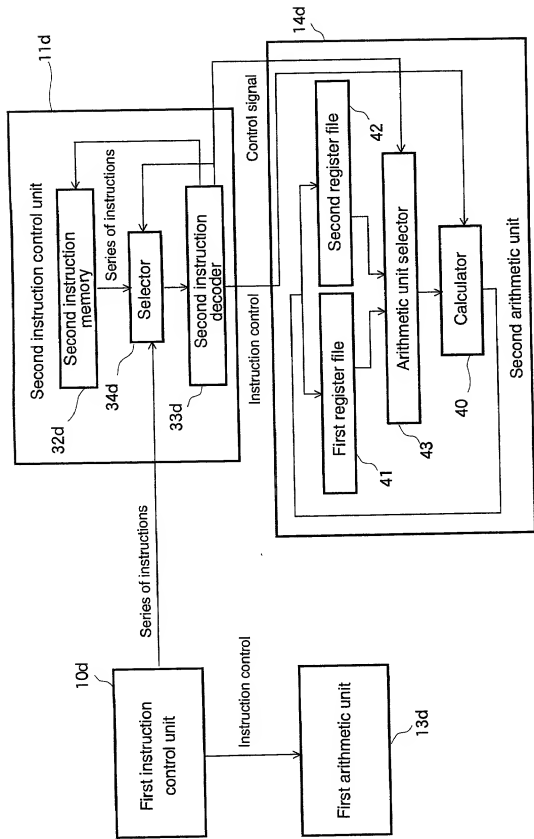


FIG. 12

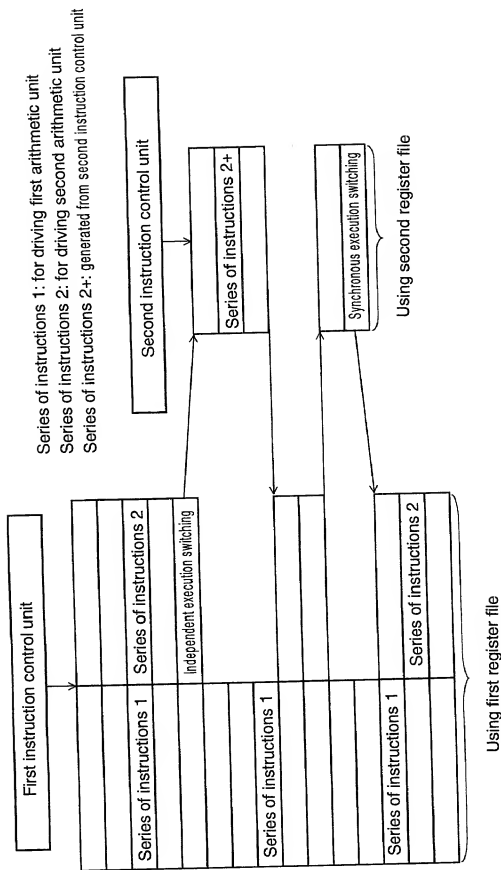


FIG. 13

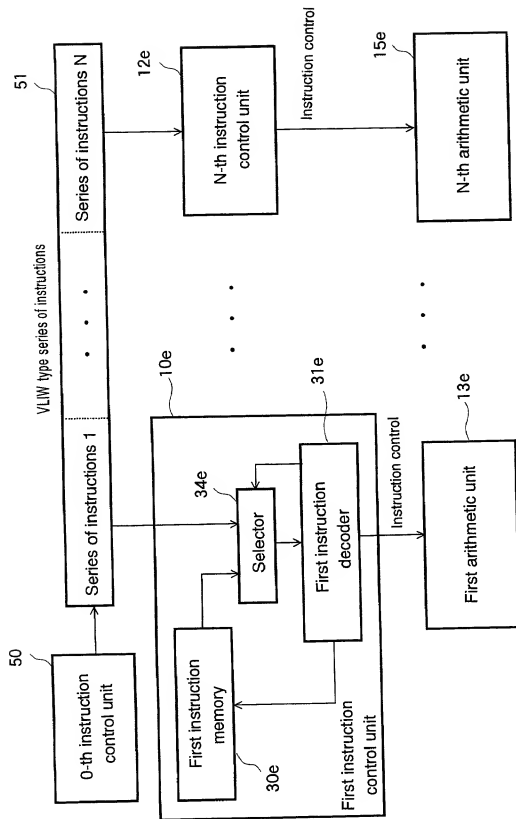


FIG. 14

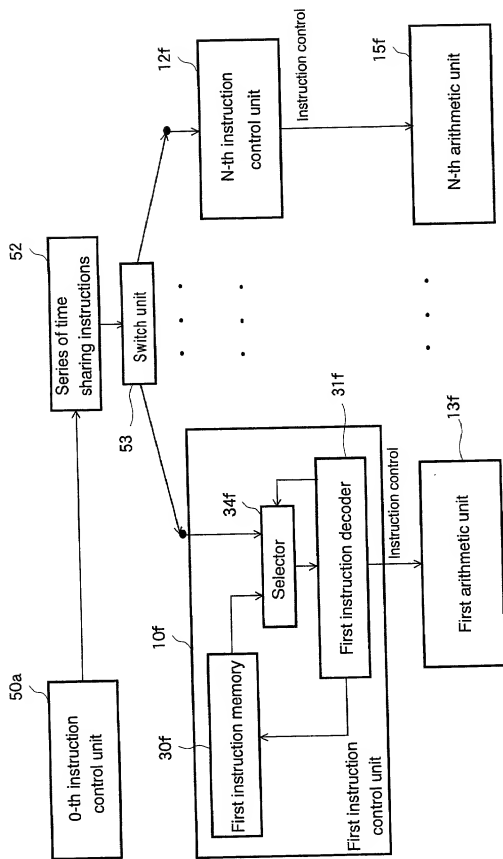


FIG. 15

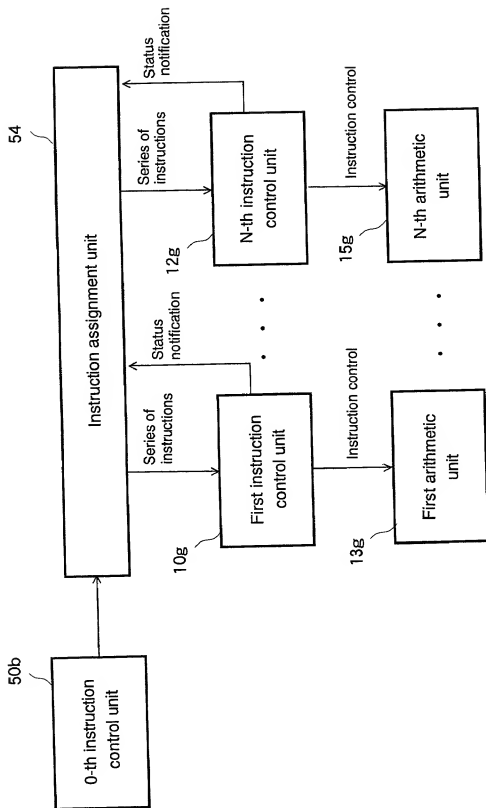


FIG. 16

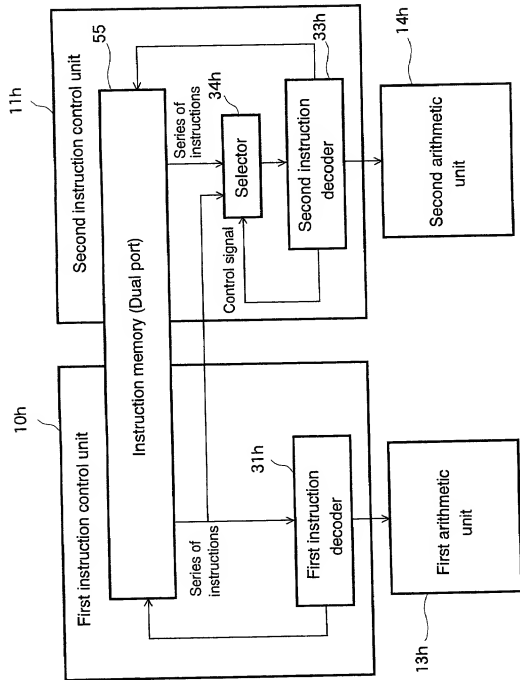




FIG. 17

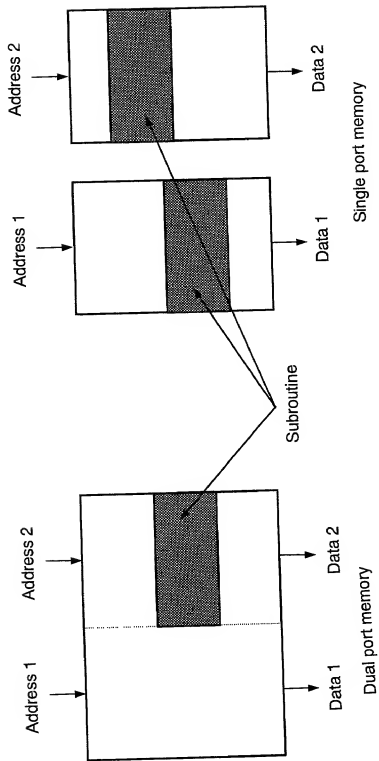


FIG. 18

